• Readings • OCAD Using the UML Copyright © 1994-1998 Rational Software, all rights reserved • will post ...

COMPUTER Jackson System Development (JSD)

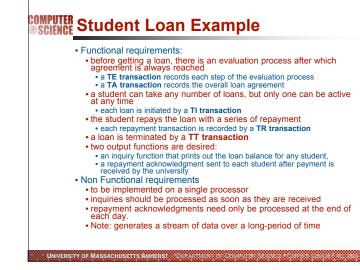
Phases

- the modeling phase
- Entity/action step
- Entity structure step
- Model process step
- the network phase
 - connect model processes and functions in a single system specification diagram (SSD)
- implementation phase

UNIVERSITY OF MASSACHUSETTS AMHERST

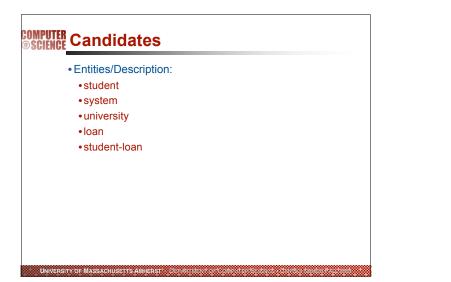
- examine the timing constraints of the system
- consider possible hardware and software for implementing our system
- design a system implementation diagram (SID)

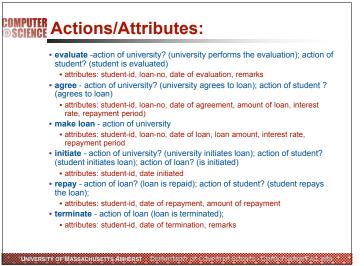
UNIVERSITY OF MASSACHUSETTS AMHERST + DEPARTMENT OF COMPUTER SCIENCE + CMPSICID20620FALL

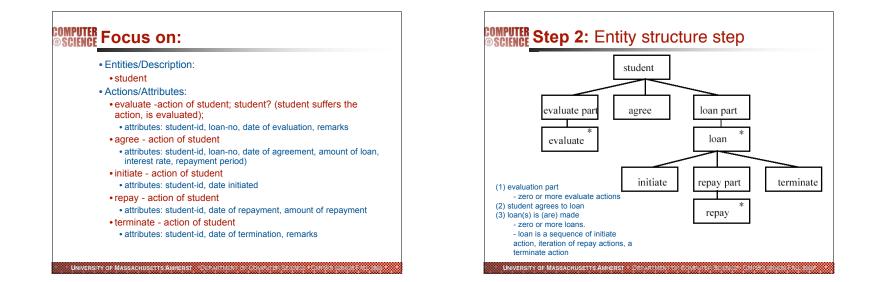


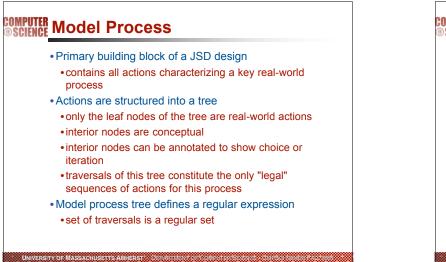
Step 1: Entity/action step Actions have the following characteristics: an action takes place at a point in time an action must take place in the real world outside of the system. an action is atomic, cannot be divided into subactions. Entities have the following characteristics: an entity performs or suffers actions in time. an entity must exist in the real world, and not be a

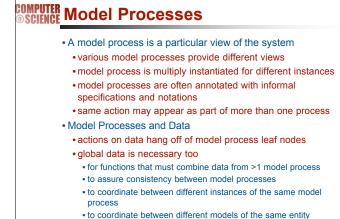
construct of a system that models the real world
an entity must be capable of being regarded as an individual; and, if there are many entities of the same type, of being uniquely named.



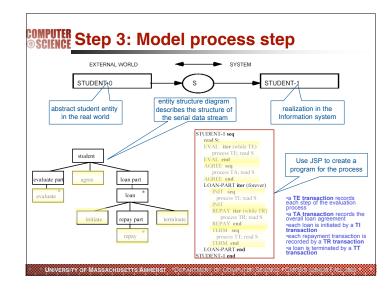


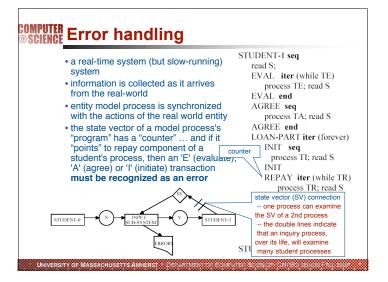


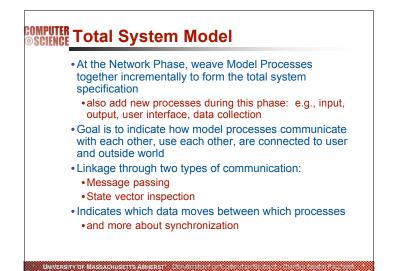




UNIVERSITY OF MASSACHUSETTS AMHERST · DEPARTMENT OF COMPUTER SCIE







COMPUTER Model Process Communication

- Fundamental notion is Data Streams
 - can have multiple data streams arriving at an action in a process
 - can model multiple instances entering a data stream or departing from one
- Two types of data stream communication:
- asynchronous message passing
- State vector inspection
- These communication mechanisms used to model how data is passed between processes

UNIVERSITY OF MASSACHUSETTS AMHERST DEPARTMENT OF COMPUTER SCIENCE - CMPScrobulgerFac. 20

COMPUTER Message Passing

- Data stream carries a message from one process activity to an activity in another process
- must correlate with output leaf of sending model process
 must correlate with input leaf of receiving model process
- Data transfer assumed to be asynchronous
- less restrictive assumption
- no timing constraints are assumed
- messages are queued in infinitely long queues
- messages interleaved non-deterministically when multiple streams arrive at same activity

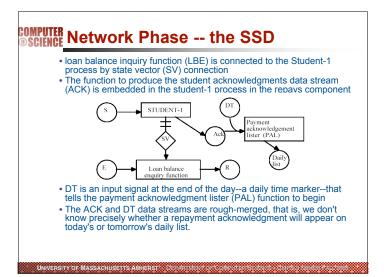
COMPUTER State Vector Inspection

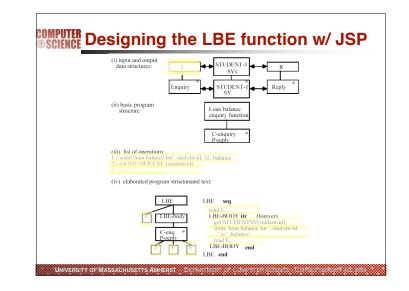
- Modeling mechanism used when one process needs considerable information about another
- State vector includes

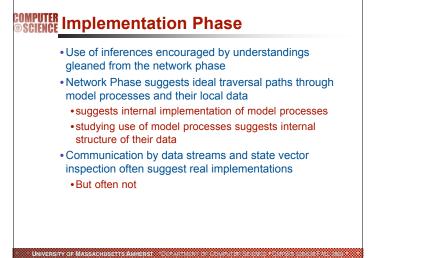
UNIVERSITY OF MASSACHUSETTS AMHERST · DEP

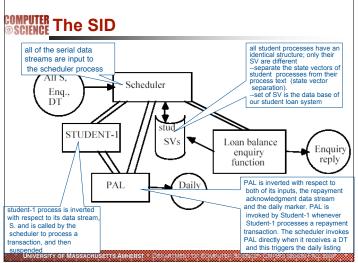
- •values of all internal variables
- execution text pointer
- Process often needs to control when its state vector can be viewed
- process may need exclusive access to its vector
- Could be modeled as message passing, but important to underscore characteristic differences

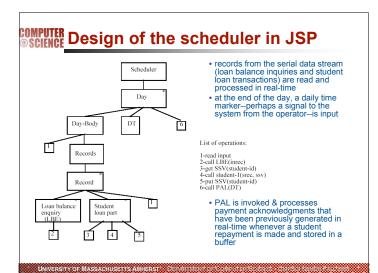
UNIVERSITY OF MASSACHUSETTS AMHERST · DEPARTIN











COMPUTER JSD and JSP • In JSD, the principles of JSP are extended into the areas of systems analysis, specification, design and implementation . In JSP, a simple program describes a sequential process that communicates by means of sequential data streams; its structure is determined by the structure of its input and output data stream • In JSD, the real world is modeled as a set of sequential model processes that communicate with the real world and with each other by sequential data streams (as well as by a second read-only communication called state vector connection). The structure of a model process is determined by the structure of its inputs and outputs • The JSD implementation step embodies the JSP implementation technique, program inversion, in which a program is transformed into a procedure • Other JSP techniques, such as the single read-ahead rule and backtracking, and principles, such as implementation through transformation, are used in JSD

UNIVERSITY OF MASSACHUSETTS AMHERST DEPARTMENT OF COMPUTER SCIENCE - CMPScrosofbare act, and

COMPUTER Comments/Evaluation

- Focus on conceptual design
- •But difficult to build a system this way
- Based upon model of real world
- Careful (and experienced) analysis of the model generally points suggested implementation tactics, though
- Parnas notions of module not perceptible here
 Not an iterative refinement approach either
- Treatment of data is very much subordinated/secondary
- Does a good job of suggesting possible parallelism
- Contrasts strongly with Objected Oriented notions (eg. Booch, UML)

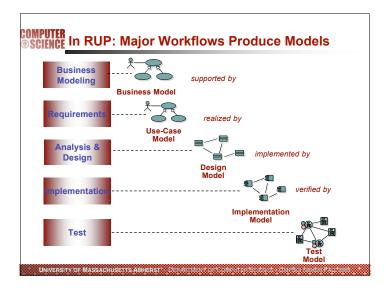
COMPUTER Rational Unified Process

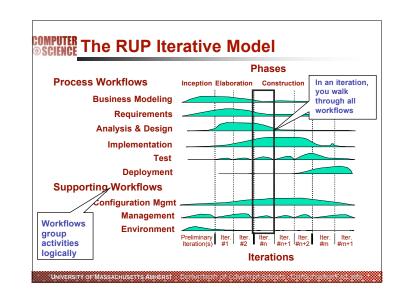
- The Unified Modeling Language (UML) is a language for specifying, visualizing, constructing, and documenting the artifacts of a software-intensive system
- A software development process defines Who is doing What, When and How in building a software product
- The Rational Unified Process has four phases: Inception, Elaboration, Construction and Transition
- Each phase ends at a major milestone and contains one or more iterations
- An iteration is a distinct sequence of activities with an established plan and evaluation criteria, resulting in an executable release

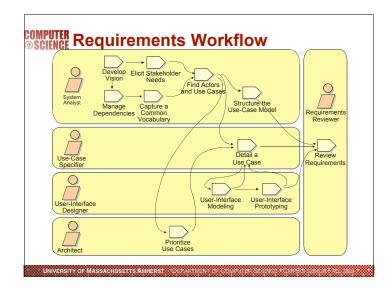
The RUP presentation is adapted from **OOAD Using the UML** Copyright 1994-1998 Rational Software, all rights reserved

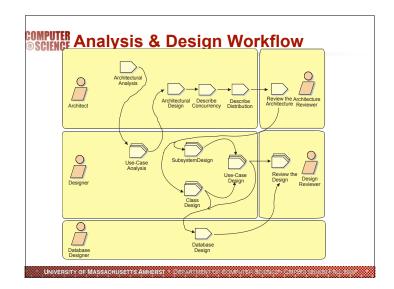
UNIVERSITY OF MASSACHUSETTS AMHERST

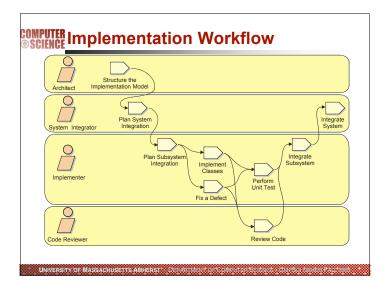
UNIVERSITY OF MASSACHUSETTS AMHERST

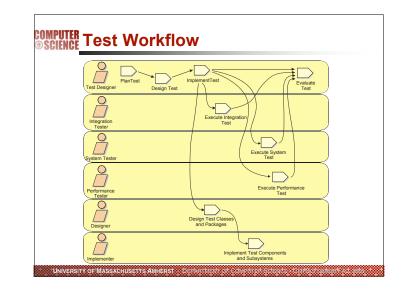


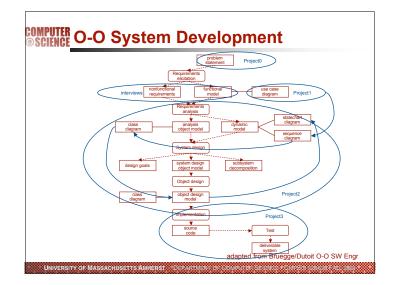










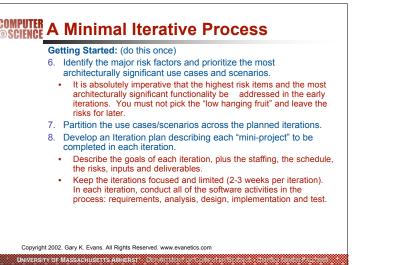


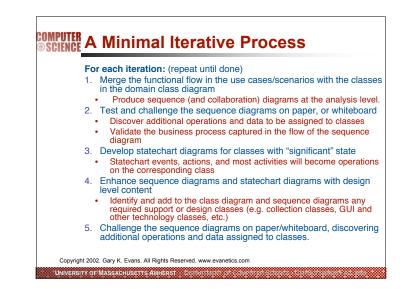
COMPUTER A Minimal Iterative Process

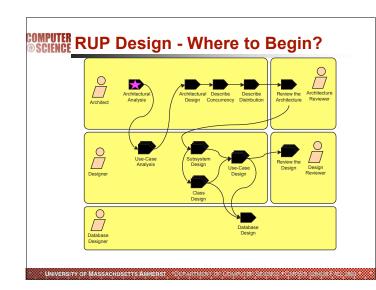
Getting Started: (do this once)

- 1. Capture the major functional and non-functional requirements for the system.
- Express the functional requirements as use cases, scenarios, or stories.
- Capture non-functional requirements in a standard paragraph-style document.
- 2. Identify the classes which are part of the domain being modeled.
- 3. Define the responsibilities and relationships for each class in the domain.
- 4. Construct the domain class diagram.
- This diagram and the responsibility definitions lay a foundation for a common vocabulary in the project.
- 5. Capture use case and class definitions in an OO CASE tool (e.g., Rose) only when they have stablilized.

Copyright 2002. Gary K. Evans. All Rights Reserved. www.evanetics.com UNIVERSITY OF MASSACHUSETTS AMHERST DEPARTMENT OF COMP



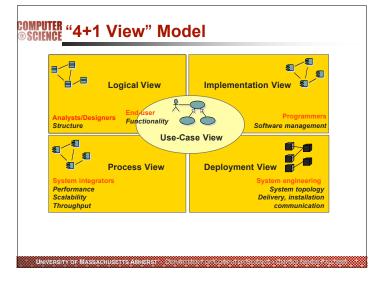


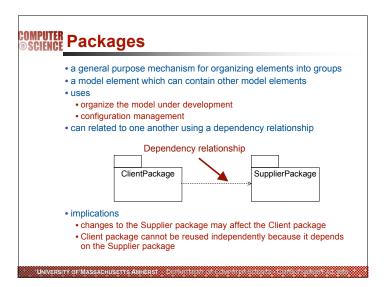


Architectural Analysis Topics Key Architectural Analysis Concepts Modeling Conventions Analysis Mechanisms Key System Concepts Initial Architectural Layers Architectural Analysis Checkpoints

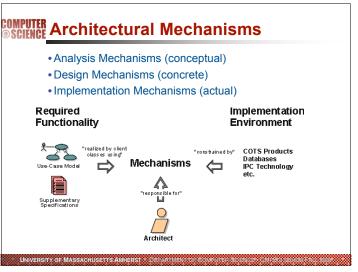
UNIVERSITY OF MASSACHUSETTS AMHERST

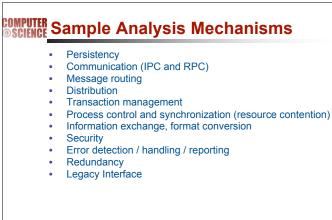
©Rick Adrion 2004 (except where noted)





COMPUTER Example: Modeling Conventions Use Case View • Use Cases will be named with short active phrases such as "Submit Grades" Logical View Required •A Use Case Realization package will be created that Functionality includes: • At least one realization per use case traced to the use case £-"realized by client classes using" 6 \sim • A "View Of Participating Classes" diagram that shows the Use-Case Model participants in the realization and their relevant relationships Classes will be named with noun names matching the problem domain as much as possible. Supplementary Specifications UNIVERSITY OF MASSACHUSETTS AMHERST



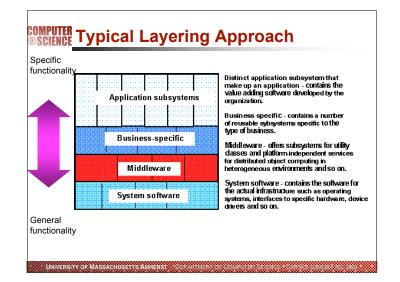


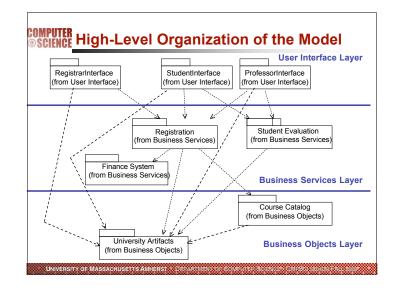
UNIVERSITY OF MASSACHUSETTS AMHERST + DEPARTMENT OF COMPUTER SCIENCE + CMPSCI 320/620/F/

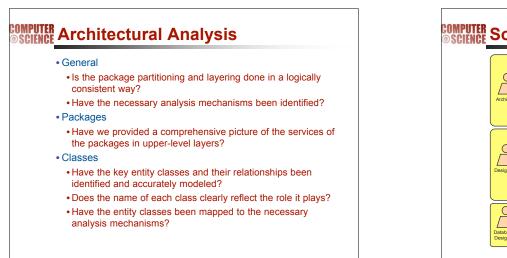
COMPUTER Identify Key Concepts • Define preliminary entity analysis classes Domain knowledge Requirements Glossary • Domain Model, or the Business Model (if exists) Define analysis class relationships Model analysis classes and relationships on Class Diagrams Include brief description of analysis class Map analysis classes to necessary analysis mechanisms

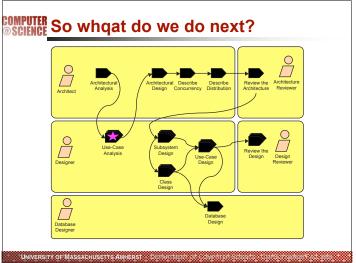
Analysis classes will evolve

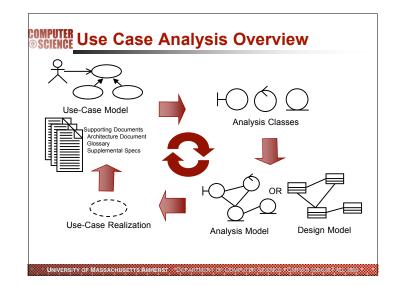
UNIVERSITY OF MASSACHUSETTS AMHERST + DEPARTMENT OF COMPUTER SCIENCE + CMPSICID20620FALL



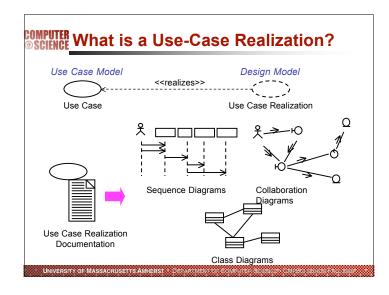








UNIVERSITY OF MASSACHUSETTS AMHERST - DEPARTMENT OF COMPUTER SCIENCE - CMPSOI 520820 FALL



COMPUTER Use Case Analysis Steps

• For each use case realization

• Supplement the Descriptions of the Use Case

UNIVERSITY OF MASSACHUSETTS AMHERST · DEPARTMENT OF COMPUTER SCIENCE · CMPSQI620/Fac

